SEMICONDUCTOR MANUFACTURING INTERNATIONAL CORP
Form 6-K
April 24, 2006

SECURITIES AND EXCHANGE COMMISSION

Washington, D.C. 20549

FORM 6-K

REPORT OF FOREIGN PRIVATE ISSUER

Pursuant to Rule 13a-16 or 15d-16

under the Securities Exchange Act of 1934

For the month of April 2006

Commission File Number 1-31994

SEMICONDUCTOR MANUFACTURING INTERNATIONAL CORPORATION

(Translation of Registrant s Name Into English)

18 Zhangjiang Road

Pudong New Area, Shanghai 201203

People s Republic of China

(Address of Principal Executive Offices)

(Indicate by check mark whether the registrant files or will file annual reports under cover of Form 20-F or Form 40-F):

Form 20-F __X__ Form 40-F _____

(Indicate by check mark if the registrant is submitting the Form 6-K in paper as permitted by Regulation S-T Rule 101(b)(1)):

Yes _____ No __X__

(Indicate by check mark if the registrant is submitting the Form 6-K in paper as permitted by Regulation S-T Rule 101(b)(7)):

Yes _____ No __X__

(Indicate by check mark whether by furnishing the information contained in this Form, the registrant is also thereby furnishing the information to the Commission pursuant to Rule 12g3-2(b) under the Securities Exchange Act of 1934):

Yes _____ No __X__

(If Yes is marked, indicate below the file number assigned to the registrant in connection with Rule 12g3-2(b): 82-_____)

Semiconductor Manufacturing International Corporation (the Registrant) is furnishing under the cover of Form 6-K:

Exhibit 99.1: Press release, dated April 12, 2006, entitled SMIC and Cadence Deliver New Analog Mixed-Signal Reference Flow to Speed Fabless Chip Design.

SIGNATURE

Pursuant to the requirements of the Securities Exchange Act of 1934, the Registrant has duly caused this report to be signed on its behalf by the undersigned, thereunto duly authorized.

Semiconductor Manufacturing
International Corporation

By: /s/ Richard R. Chang
Name: Richard R. Chang
Title: President and Chief Executive Officer

Date: April 24, 2006

EXHIBIT INDEX

Exhibit	Description
Exhibit 99.1:	Press release, dated April 12, 2006, entitled SMIC and Cadence Deliver New Analog Mixed-Signal Reference Flow to Speed Fabless Chip Design.

Exhibit 99.1

SMIC AND CADENCE DELIVER NEW ANALOG MIXED-SIGNAL

REFERENCE FLOW TO SPEED FABLESS CHIP DESIGN

New Jointly Designed Reference Flow Improves Designer Productivity for

Consumer, Networking and Wireless Markets

SAN JOSE, Calif. and SHANGHAI, China April 12, 2006 Cadence Design Systems, Inc. (NASDAQ: CDNS) and Semiconductor Manufacturing International Corporation (SMIC , NYSE: SMI; SEHK: 0981.HK) today announced that the companies have jointly developed an analog mixed-signal (AMS) reference flow to address the needs of designers developing ICs for the consumer, networking and wireless markets.

The block-level reference flow is based on SMIC s 0.18um multi-mode, radio-frequency process design kit (PDK) and the Cadence® Virtuoso® custom design platform and Design for Manufacturing (DFM) technologies. The flow has been proven through silicon and package validation of a sample analog-to-digital converter (ADC) design. The flow improves designer productivity by providing a reference design environment, baseline flow and an example design demonstrating how designers can use SMIC s process technology and the Cadence Virtuoso platform.

Our collaboration with Cadence helps to drive our goal of continuing to enable the Chinese semiconductor market, said Paul Ouyang, vice president of Design Services, SMIC. As a leader in analog mixed-signal design solutions, Cadence has provided its unique technology and expertise to create this reference flow. This solution will help to facilitate analog mixed-signal designs for the growing consumer, networking and wireless markets, particularly in China.

Cadence and SMIC aim to continue to collaborate on improving designers productivity by focusing on mainstream and advanced process technologies, said Mike McAweeney, vice president of business development of Industry Alliances at Cadence.

Cadence has a strong worldwide foundry access team that works with leading foundries to develop PDKs and reference flows that enable customers to speed up product development cycles and reduce design failure risks. The AMS reference flow is a result of the continuing efforts of Cadence and SMIC to promote customer success through collaboration.

The SMIC-Cadence analog mixed-signal reference flow, based on OpenAccess 2.2, the industry open database standard, provides designers an optimized and predictable schematic-to-GDSII flow that has been proven from design, layout, silicon, and measurements. The flow provides a starting point for design teams creating SoCs or putting together a flow of their own. The flow incorporates several Cadence technologies, including Virtuoso Spectre® Circuit Simulator, Virtuoso UltraSim Full-chip Simulator, Virtuoso Schematic Editor, Virtuoso Analog Design Environment, Virtuoso Specification-driven Environment, Virtuoso Chip Assembly Router, Virtuoso XL Layout Editor, Assura Design Rule Checker (DRC) / Layout vs. Schematic (LVS) Verifier and Cadence QRC Extraction.

Cadence has a strong worldwide foundry enabling team that works with leading foundries, such as SMIC, to develop process design kits and reference flows that enable our mutual customers to speed up product development cycles and reduce design failure risks, said Srinivas Raman, VP Research & Development of Virtuoso® Platform at Cadence. The AMS reference flow is a result of the continuous efforts of Cadence and SMIC to support customer success through partnering.

Availability

The SMIC and Cadence AMS reference flow kit is available to SMIC customers who may request the reference flow by contacting SMIC s Design Services at design_services@smics.com.

About SMIC

Semiconductor Manufacturing International Corporation, (SMIC , NYSE: SMI and HKSE: 0.981.HK), headquartered in Shanghai, China, is an international company and one of the leading semiconductor foundries in the world, providing integrated circuit (IC) manufacturing at 0.35um to 90nm and finer line technologies to customers worldwide. Established in 2000, SMIC has four 8-inch wafer fabrication facilities in volume production in Shanghai and Tianjin. In the first quarter of 2005, SMIC commenced

commercial production at its 12-inch wafer fabrication facility in Beijing, the first 12-inch fab in China. SMIC also maintains customer service and marketing offices in the U.S., Europe, and Japan, and a representative office in Hong Kong. SMIC s pool of talents includes over 2,500 semiconductor industry experts and technical staff. SMIC has achieved ISO9001, ISO/TS16949, OHSAS18001, TL9000, BS7799 and ISO14001 certifications. For additional information, please visit http://www.smics.com.

About Cadence

Cadence enables global electronic-design innovation and plays an essential role in the creation of today s integrated circuits and electronics. Customers use Cadence software and hardware, methodologies, and services to design and verify advanced semiconductors, printed-circuit boards and systems used in consumer electronics, networking and telecommunications equipment, and computer systems. Cadence reported 2005 revenues of approximately $1.5 billion, and has approximately 5,000 employees. The company is headquartered in San Jose, Calif., with sales offices, design centers, and research facilities around the world to serve the global electronics industry. More information about the company, its products, and services is available at www.cadence.com.

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Safe Harbor Statements

(Under the U.S. Private Securities Litigation Reform Act of 1995)

Certain statements contained in this press release, such as statements concerning future collaboration between SMIC and Cadence, may be viewed as forward-looking statements within the meaning of Section 27A of the U.S. Securities Act of 1933, as amended, and Section 21E of the U.S. Securities Exchange Act of 1934, as amended. Such forward-looking statements involve known and unknown risks, uncertainties and other factors (including without limitation the actual results of any future collaborative efforts between SMIC and Cadence), which may cause actual events, and/or the actual performance, financial condition or results of operations of SMIC to be materially different from any future performance, financial condition or results of operations implied by such forward-looking statements. Further information regarding these risks, uncertainties and other factors is included in SMIC s annual report on Form 20-F filed with the U.S. Securities and Exchange Commission (the SEC) on June 28, 2005 and such other documents that SMIC may file with the SEC or The Stock Exchange of Hong Kong Limited from time to time.

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